

FEB 20 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

First-Named Inventor: JOSE DE JESUS PINEDA
DE GYVEZ

Application No.: 10/523,666 Conf.: 8446

Date Filed: 02/04/2005

Atty Docket No.: NL020835US

Art Unit: 2816

Examiner: TRA ANH QUAN

Title: CIRCUIT AND METHOD FOR CONTROLLING THE THRESHOLD VOLTAGE OF TRANSISTORS

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

NOTICE OF APPEAL

Sir:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision dated 11/20/2006 of the Examiner finally rejecting claims 1, 4-10.

☒ Please charge the fee of \$500.00 to Deposit Account No. 50-4019.

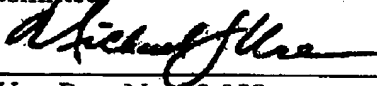
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Date: 2007-FEB-20

Respectfully submitted,

By


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RE:	Serial No.: 10/523,666
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